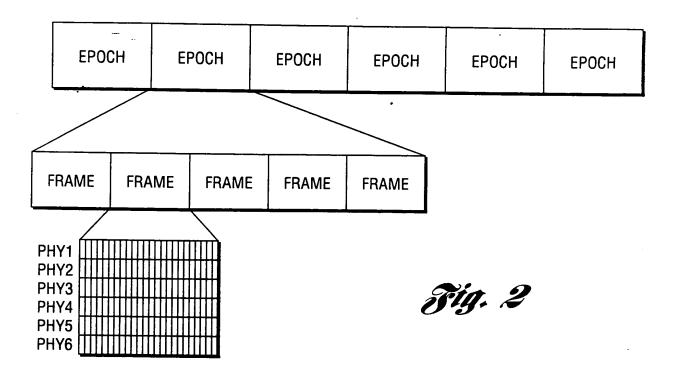


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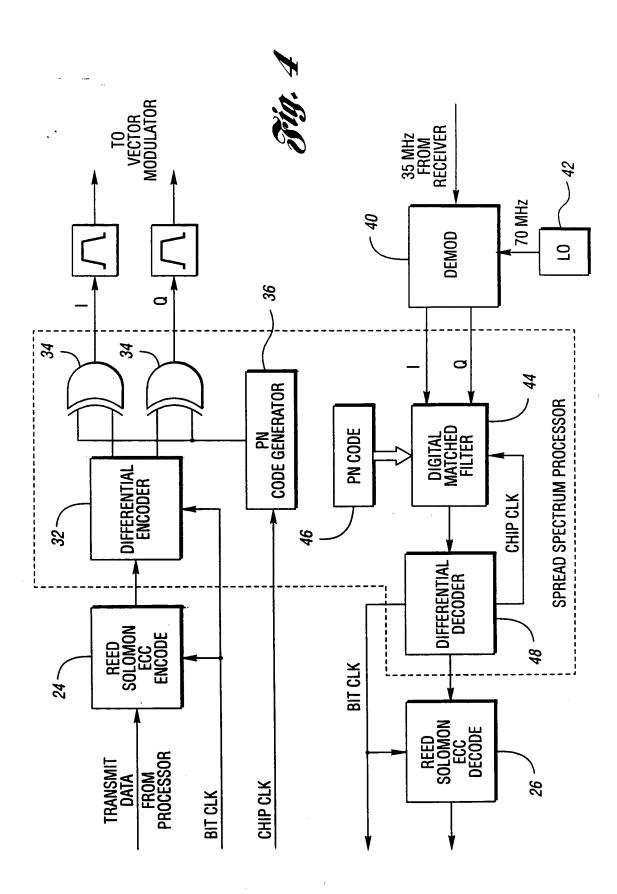
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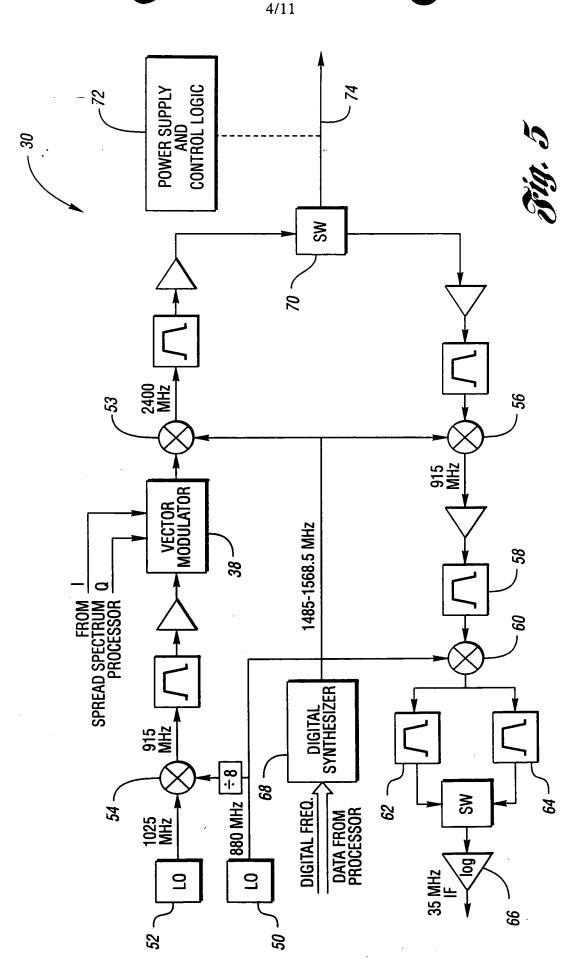


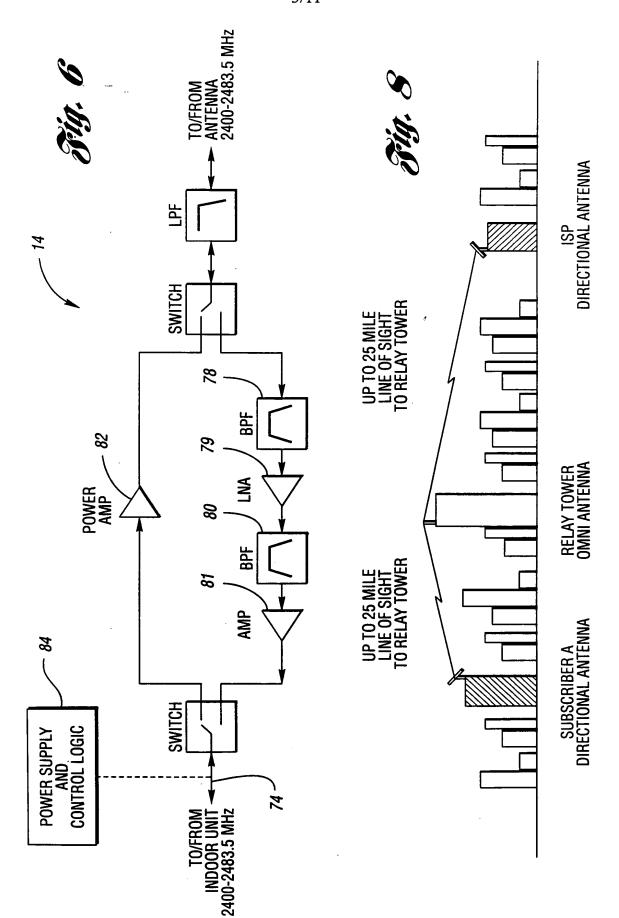
TUNING TIME ALLOCATION [µS]	GUARD: SYNCHRONIZATION ALLOWANCE [µS]	ACQUISITION SYMBOL [CHIPS]	HEADER [BYTES]	DATA BLOCK (PAYLOAD) [BYTES]	ERROR CORRECTION CODING FOR DATA BLOCK (BYTES)						RANGE DELAY ALLOCATION [25 mi]
Т	G	Α	Н	DATA BLOCK ₁ [BYTES]	ECC [BYTES]	•••	Α	Н	DATA BLOCK _B [BYTES]	ECC [BYTES]	R

Fig. 3

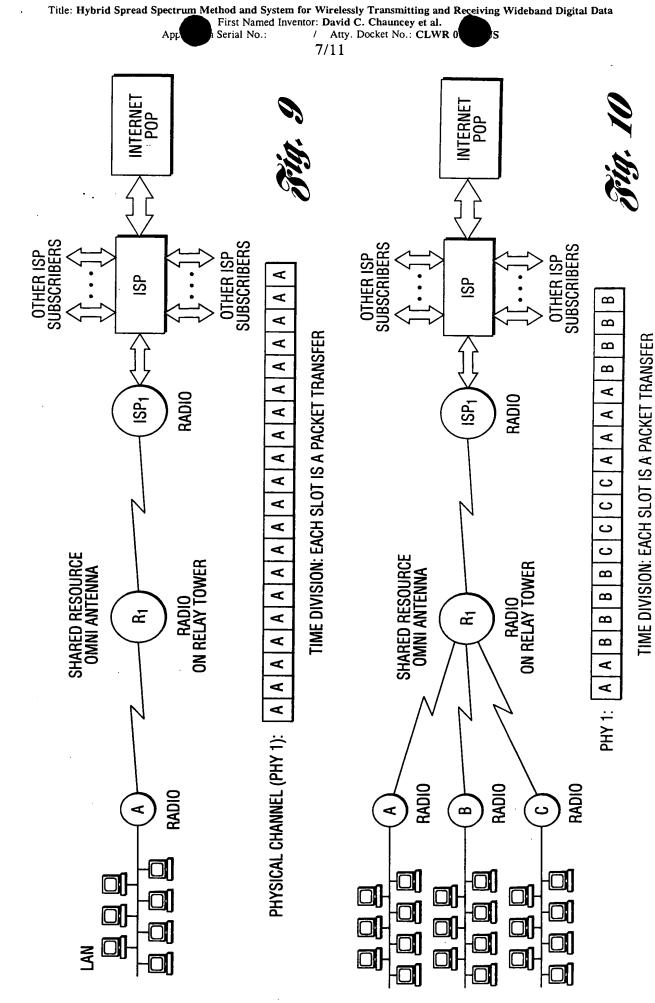
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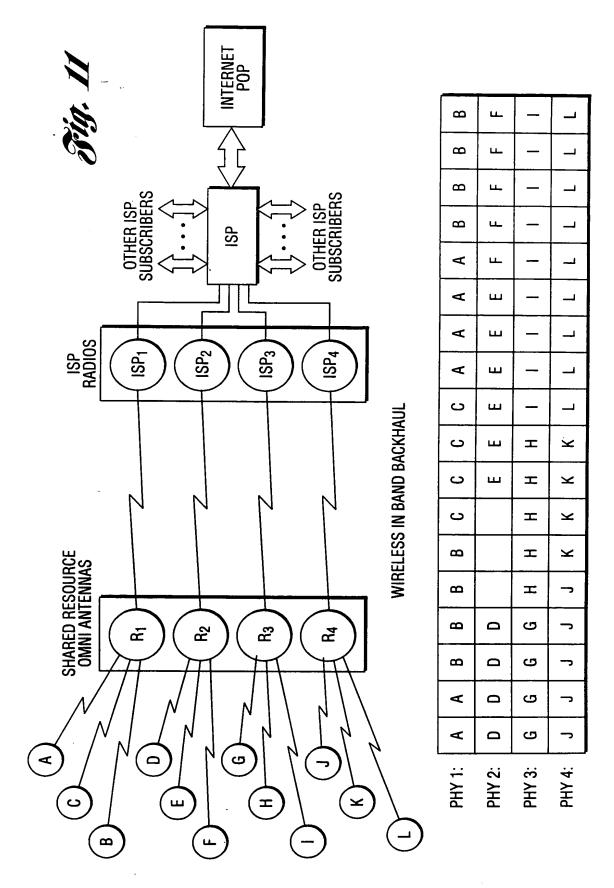


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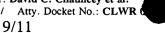
First Named Inventor: David C. Chauncey et al.
Serial No.: / Atty. Docket No.: CLWI

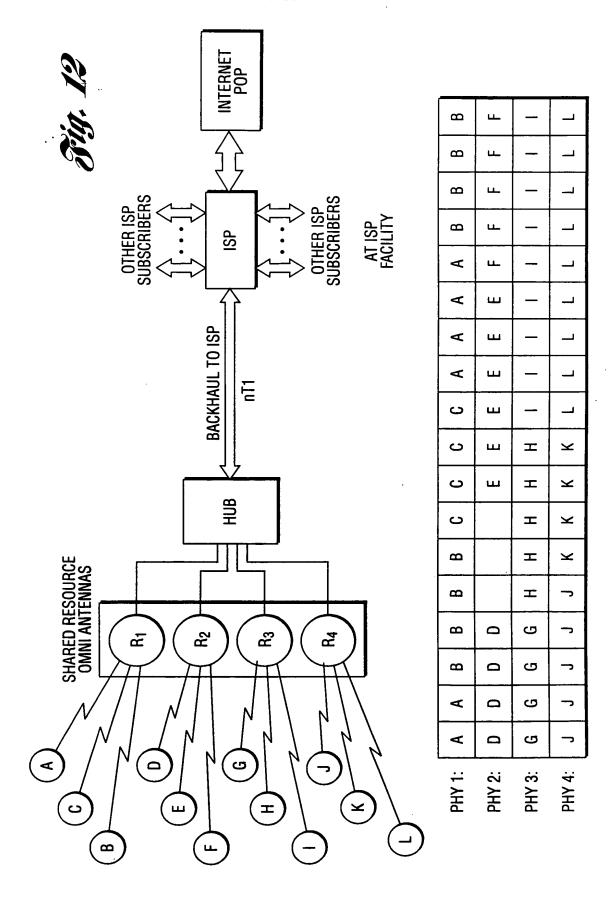
/ Atty. Docket No.: CLWR 0

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Title: Hybrid Spread Spectrum Method and System for Wirelessly Transmitting and Receiving Wideband Digital Data
First Named Inventor: David C. Chauncey et al.
Approximately a special No.: / Atty. Docket No.: CLWR 10/11

Fig. 13

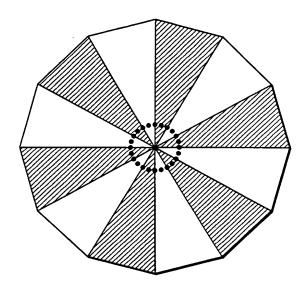


Fig. 14

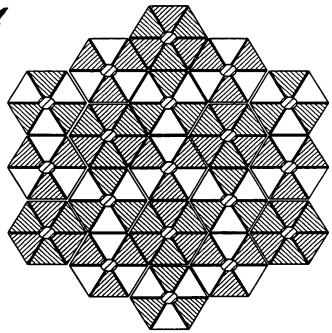


Fig. 15

